

WHAT IS CLAIMED IS:

Cix		1.	A mobile computing system comprising of:			
2	a communication device;					
3	' /	a perso	sonal computing system (PC) comprised of			
4			a storage device capable of receiving and storing messages from the			
5			communication device; and			
6		a perso	onal digital assistant system (PDA) comprised of			
7			a storage device capable receiving and storing messages from the			
8			communication device, whereby the storage device of the PC is capable of			
9		synchronizing received messages with the storage device of the PDA.				
1	-	2.	The mobile computing system of claim 1 wherein the storage device of the PC			
2	2 \(\frac{1}{2} \) is a memory array comprised of a set of records, and the storage device of the PDA is a					
3	memory array comprised of a set of records.					
[i i					
1	Ų.	3.	The mobile computing system of claim 2 wherein a direct correspondence is			
2 "	2 is established between the set of records of the PC memory array and the set of records of the					
3	3 PDA memory array.					
1 .		4.	The mobile computing system of claim 2 wherein messages are synchronized			
2	2 between the memory array of the PC and the memory array of the PDA.					
1		5.	The mobile computing system of claim 3 wherein messages are synchronized			
2	betwee	en recor	ds of the PC memory array and records of the PDA memory array.			
1		6.	The mobile computing system of claim 1 wherein the storage device of the PC			

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is a hard disk drive.

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1	7.	The mobile computing system of claim 6 wherein the hard disk drive is		
2	comprised of a	a memory array, and the PDA storage device is comprised of a memory array,		
3	wherein the PC	hard disk drive memory array corresponds directly to the PDA memory array		
1	8.	A mobile computing system comprising of:		
2	a comm	nunication device;		
3	a person	nal computing system (PC) capable of receiving messages through the		
4		communication device; and		
5	a person	nal digital assistant system (PDA) capable of receiving messages through the		
6		communication device and synchronizing the messages with the PC.		
1	9.	The mobile computing system of claim 8 wherein the PDA is further		
		memory array where messages are received and entered, and the memory		
3	array is synchro	onized to the PC.		
1	10.	The mobile computing system of claim 9 wherein the PC is further comprised		
	1	ray that is synchronized to the memory array of the PDA.		
	. —	The mobile computing system of claim 9 wherein the PC is further comprised		
2	of a hard disk o	lrive that is synchronized to the memory array of the PDA.		
1	12.	A method of clearing and archiving messages in a dual system computer		
2	architecture con	mprised of:		
3	receivin	g and storing messages by a first computer system to a first memory device;		
4	synchro	nizing the messages with a second computer system, whereby the second		
5		computer system archives synchronized messages to a second memory device;		
6		and		
7	deleting	synchronized and archived messages whenever the first memory device is		
8	;	filled.		



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1	13.	The method of clean	ng and archiving messages in a dual system computer
2		architecture of claim	12 further comprising:

identifying the deleted messages in the first memory devices.

- 14. The method of clearing and archiving messages in a dual system computer architecture of claim 12 wherein the first computer system is a personal digital assistant system (PDA) and the second computer system is a personal computer system (PC).
- 15. The method of clearing and archiving messages in a dual system computer architecture of claim 13 wherein the first computer system is a personal digital assistant system (PDA) and the second computer system is a personal computer system (PC).
- 16. A method of clearing and archiving messages in a dual system computer architecture comprised of:

receiving and storing messages by a first computer system to a first memory device; synchronizing the messages with a second computer system, whereby the second computer system archives synchronized messages to a second memory device; and

informing a user whenever the first memory device is filled.

- 17. The method of clearing and archiving messages in a dual system computer architecture of claim 14 further comprised of:
 - deleting messages from the first memory device after the messages have been read by the user.
- 18. The method of clearing and archiving messages in a dual system computer architecture of claim 16 wherein the first computer system is a personal digital assistant (PDA) and the second computer system is a personal computer system (PC).
- 19. The method of clearing and archiving messages in a dual system computer architecture of claim 17 wherein the first computer system is a personal digital assistant (PDA) and the second computer system is a personal computer system (PC).

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1	1 20. The method of clearing and arc	hiving messages in a dual system computer					
2	2 architecture of claim 12 further comprised of:	architecture of claim 12 further comprised of:					
3	3 setting preferences as to received and s	tored messages.					
1	1 21. The method of clearing and arc	hiving messages in a dual system computer					
2	2 architecture of claim 13 further comprised of:	architecture of claim 13 further comprised of:					
3	3 setting preferences as to received and s	setting preferences as to received and stored messages.					
1	1 22. The method of clearing and arc	hiving messages in a dual system computer					
2	2 architecture of claim 14 further comprised of:						
		setting preferences as to received and stored messages.					
1	The method of clearing and arc	hiving messages in a dual system computer					
	2 architecture of claim 15 further comprised of:						
3	setting preferences as to received and s	setting preferences as to received and stored messages.					
1	1 24. The method of clearing and arc	hiving messages in a dual system computer					
	2 architecture of claim 16 further comprised of:						
	setting preferences as to received and s	tored messages.					
1	1 25. The method of clearing and arc	hiving messages in a dual system computer					
2	2 architecture of claim 17 further comprised of	architecture of claim 17 further comprised of					
3	3 setting preferences as to received and s	tored messages.					
1	1 26. The method of clearing and arc	niving messages in a dual system computer					
2	architecture of claim 18 further comprised of:						
3	3 setting preferences as to received and s	tored messages.					
1	1 27. The method of clearing and arc	hiving messages in a dual system computer					
2	architecture of claim 19 further comprised of:						
3	setting preferences as to received and s	tored messages.					

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